

| ISSUE CLASSIFICATION | |
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| Class | Subclass |



$\frac{J_{C1}}{J_{C23}}$

PATENT DATE

SCANNED

Q.A.

APPLICANTS

TITLE

Substrate structure of flip chip package

PTO-2040
12/89[illegible]

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|---|--|-------------|------------|-----------------------------------|----------------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |
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